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direct dial 512.370.2851
kkordzik@winstead.com

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207.184-PI40US

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	USPTO		703-872-9306

MESSAGE: Attached please find an updated Issue Fee Transmittal correcting the Assignee information. Please contact us regarding any questions or comments. Thank you.

FROM: KELLY K. KORDZIK/SB **PHONE NO.:** 512.370.2851
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SUITE 800
100 CONGRESS AVENUE
AUSTIN, TEXAS 78701

PH 512 471 4330
FAX 512 570 2550
WINSTEAD.COM

WINSTEAD SECHREST & MINICK
Attorneys and Counselors
A Professional Corporation

Austin Dallas Fort Worth
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11/28/2003

Joseph A. Sawyer Jr
Sawyer & Associates
P O Box 51418
Palo Alto, CA 94303

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2/10/04 (Date)

APPLICATION NO	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO	CONFIRMATION NO.
09/533,619	03/22/2000	Angela T. Hui	1376P/D922	7885

TITLE OF INVENTION. METHOD AND SYSTEM FOR REDUCING CHARGE GAIN AND CHARGE LOSS WHEN USING AN ARC LAYER IN INTERLAYER DIELECTRIC FORMATION

APPLN TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	03/01/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
LOUIE, WAISING	2814	438-622000

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Advanced Micro Devices, Inc.
Fujitsu Limited
Fujitsu and Semiconductor Ltd.

(B) RESIDENCE (CITY and STATE OR COUNTRY)

Sunnyvale, CA 94086
Kanagawa 211-88, Japan
Fukushima Pref. 965-845, Japan

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